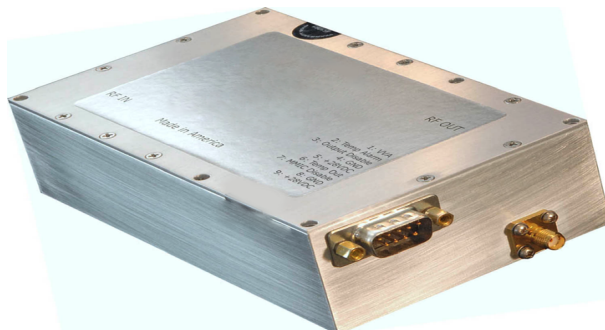


**100-500MHz 50W Class A
High Performance Amplifier
with High Speed Disable**

- ❖ **Class A 50W linear amplifier**
- ❖ **Fast output disable, $\leq 1\mu\text{sec}$**
- ❖ **VVA, over 30dB range**
- ❖ **Analog temperature out**
- ❖ **High temperature alarm**
- ❖ **Independent MMIC disable**
- ❖ **Temperature compensated bias**
- ❖ **Available with input limiter and/or heatsink and fan**



The HD31345 is a Class A high performance amplifier module, outstanding as a driver stage in military communication or radar systems. It features fast output disable with $\leq 1\mu\text{sec}$ response time. It exhibits excellent full power and back-off linearity, and utilizes a combination of three active device technologies for optimum performance and maximum ruggedness.

Specifications

$V_{\text{supply}} = +28\text{VDC}$, $I_{\text{DQ}} = 6.9\text{A}$, $P_{\text{out}} = 50\text{W}$, $T_{\text{base}} = 25^\circ\text{C}$, $Z_{\text{load}} = 50\Omega$

Parameter	Min	Typ	Max	Units
Freq. Range	100		500	MHz
$P_{1\text{dB}}$		>75		W
Input Power		-3	0	dBm
Gain	47	50		dB
Gain Flatness		+/-0.7	+/-1.5	dB
Drain Current		7.2	7.5	A
Efficiency	24	25		%
IRL		-23	-14	dB
f_2		-44	-34	dBc
f_3		-31	-23	dBc
IMD ₃ 50W PEP, $\Delta f=10\text{kHz}$ and $\Delta f=100\text{kHz}$. See Fig. 2 for 25W PEP.		-39	-32	dBc
Dimensions	3.60 X 6.55 X 1.30 (91.44 X 166.37 X 33.02)			inch (mm)

Maximum Ratings

Operation beyond these ratings may damage amplifier.

Parameter	Value
V_{supply}	24-28VDC
Bias Current	6.9A
Drain Current	8.0A
Load Mismatch*	5:1
Housing Base Temperature	65°C
Storage Temperature	-40°C to 85°C

*All phase angles, 50W forward power, current limited to 8.0A.

Option Ordering Info

Input limiter	HD31345-LIM
Heatsink and fan	HD31345-HSF

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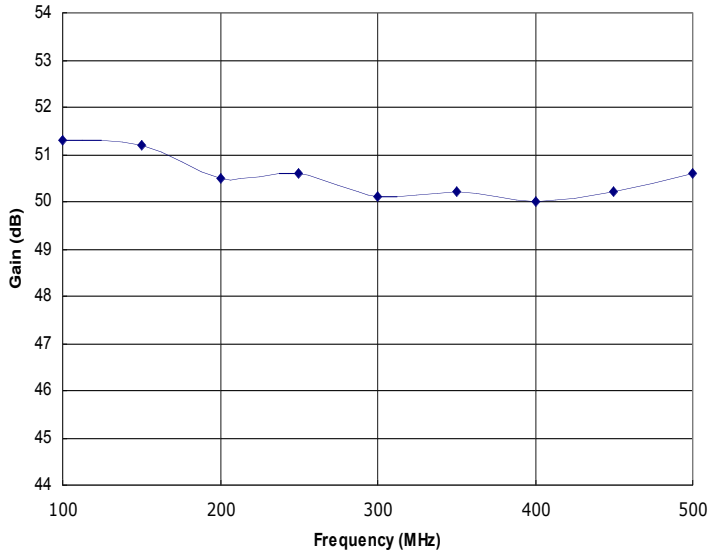


Figure 1: HD31345 Typical Gain @ $P_{out}=50W$.

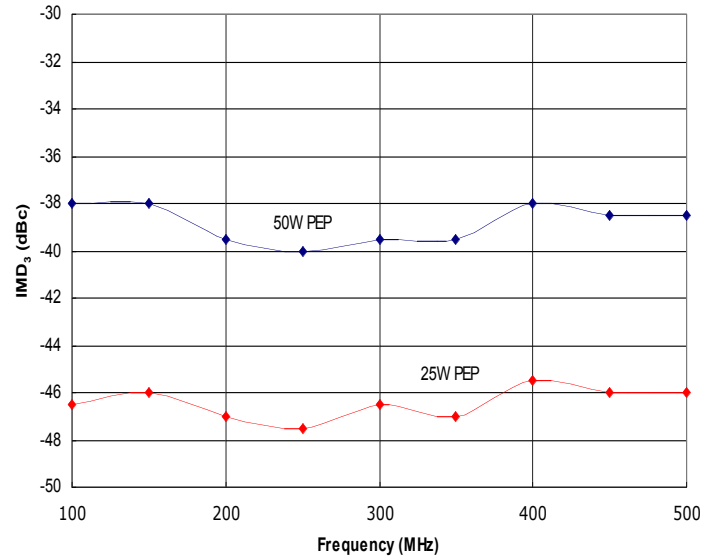


Figure 2: HD31345 Typical IMD_3 @ 50W and 25W PEP, $\Delta f=10kHz$ and $\Delta f=100kHz$.

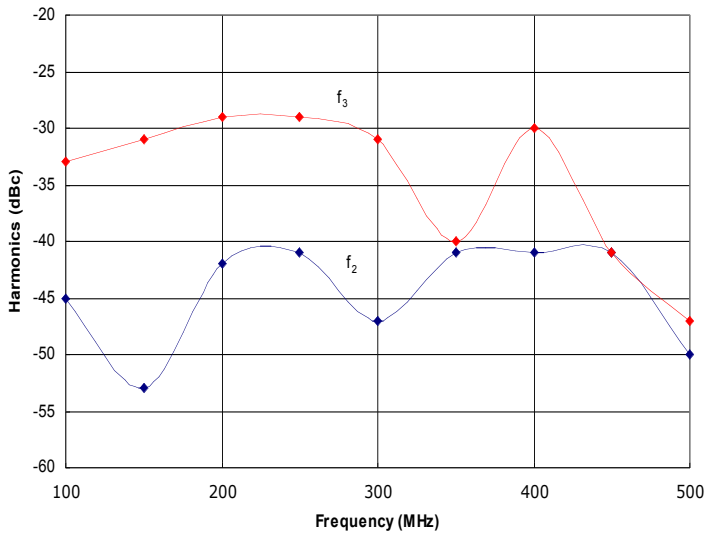


Figure 3: HD31345 Typical f_2 and f_3 @ $P_{out}=50W$.

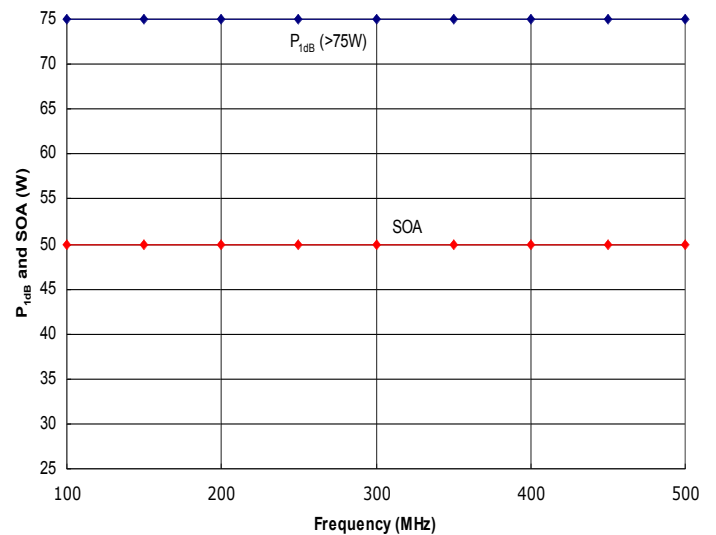
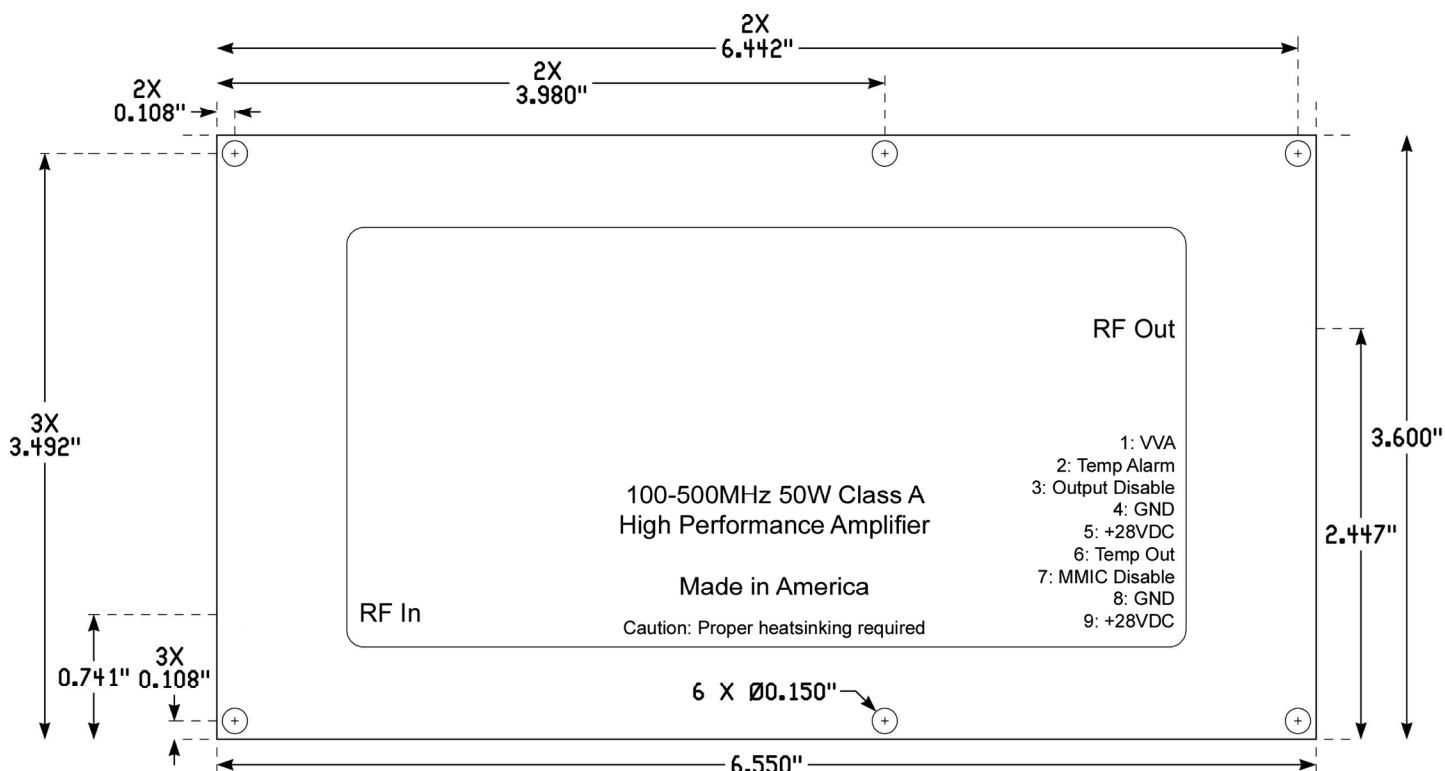


Figure 4: HD31345 Typical P_{1dB} and Safe Operating Area (SOA). The amplifier is capable of delivering much more power than it is safe to generate. Do not exceed the SOA shown above without first contacting HD Communications Corp. to discuss your application.

**100-500MHz 50W Class A
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Amplifier Mounting Hole and RF Locations



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Interface Pins and Functions

- 1: VVA** The voltage variable attenuator has a slope of approximately 30dB/volt, from 0.65 to 1.65V. Maximum attenuation is reached by 2.0V. The acceptable control range is 0 to 3.6V. **Do not exceed 3.6VDC on this pin.** Maximum RF input power is +3dBm, without the limiter option. Do not exceed the SOA in Figure 4. **The VVA control voltage should be set to zero volts during amplifier power up and power down.**
- 2: Temp Alarm** This indicates an over-temp condition, and is set to trip at a housing base temperature of approximately +65°C. It is an open drain output, pulled up internally to +15VDC, and will pull down to 0V at +65°C. The end user may either scale this range to suitable voltages with a simple resistive divider, or use an analog or digital input capable of handling +15VDC. This signal exhibits 5°C of hysteresis, and will reset to +15VDC at approximately +60°C.
- 3: Output Disable** This is active high at +5VDC, and is TTL compatible. It disables the second and output stages of the amplifier, and will reduce full rated output power to zero in $\leq 1\mu\text{sec}$. When set to 0VDC, the amplifier will return to full power in approximately 50 μsec .
- 4: GND** Amplifier ground. Connect to power supply return/ground. Use in parallel with pin #8. A minimum of 20 gauge wire is recommended.
- 5: +28VDC** Nominal supply voltage is +28VDC. As low as +24VDC may be used, with a reduction in P_{1dB} and IMD performance. Use in parallel with pin #9. A minimum of 20 gauge wire is recommended.
- 6: Temp Out** This is an analog signal that is a nominal +0.75V at +25°C. It has a positive slope of 10mV/°C with increasing temperature. An offset will exist between the temperature reported on this pin and the actual housing base temperature directly under the output transistor, until steady state conditions are reached. It is up to the end user to properly characterize this signal's response with their chosen cooling configuration.
- 7: MMIC Disable** This is a supplementary disable pin, for additional amplifier quieting at the user's discretion.
- 8: GND** Amplifier ground. Connect to power supply return/ground. Use in parallel with pin #4. A minimum of 20 gauge wire is recommended.
- 9: +28VDC** Nominal supply voltage is +28VDC. As low as +24VDC may be used, with a reduction in P_{1dB} and IMD performance. Use in parallel with pin #5. A minimum of 20 gauge wire is recommended.

Limiter Option

The limiter option significantly impacts amplifier linearity. Please contact HD Communications Corp. to learn more.

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Instructions for Amplifier Use

- 1) If not supplied with a heatsink, apply a layer of high quality thermal grease (Wakefield Type 120 or equivalent) to the underside of the amplifier housing. Thinner is better, but ensure that when mounted to your heatsink, contact across the *entire* module base is made. Gaps and air bubbles will significantly reduce cooling, leading to possible amplifier damage. Use six #6-32 screws to mount the amplifier to your heatsink. *Although not required, a high performance thermal compound (Wakefield Type 122 or equivalent) may be used, and will enhance ruggedness and extend amplifier lifetime by reducing output transistor die temperature.*
- 2) Guarantee sufficient airflow through the heatsink fins to keep the maximum housing base temperature at or less than that specified in the Maximum Ratings section. Contact HD Communications Corp. for details on how to qualify your heatsink's performance, if needed.
- 3) Connect a proper signal source to the RF IN connector, and desired load to the RF OUT connector. Torque connectors to industry standards for the type supplied with the amplifier.
- 4) Connect DC V_{supply} to pins 5 and 9 of the interface connector. Connect power supply return/ground to pins 4 and 8 of the interface connector. Ensure that V_{supply} is within the voltage range in the Maximum Ratings section.
- 5) Apply desired signals/monitoring lines to remainder of interface pins. Refer to the Interface Pins and Functions section for signal descriptions, limits, and timing requirements.
- 6) Apply DC power then sufficient RF drive to achieve desired output level. Ensure that the Safe Operating Area (SOA) power level indicated in Figure 4 is not exceeded, or amplifier damage may occur, and will void the warranty.
- 7) To disconnect the amplifier, first remove the RF drive, then DC power, then the RF connections.

Contact us at sales@rfcomp.com with any questions, or for special options, testing requirements, and/or operating conditions not specified in this document.

Document Control

Revision	Date	Notes
A	8-10-2016	Initial release.